



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:  
Fernando Gonzalez et al.

Serial No.: 10/751,141

Filed: December 31, 2003

For: Transistor Having Vertical Junction Edge and Method of Manufacturing the Same

§ Group Art Unit: 2815  
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§ Examiner: Nguyen, Joseph H.  
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§  
§ Atty. Docket: MICS:0114  
§ 02-1010  
§

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**CERTIFICATE OF TRANSMISSION OR MAILING**  
**37 C.F.R. 1.8**

**NOTICE OF APPEAL UNDER 37 C.F.R. § 41.31 FROM THE DECISION OF THE  
EXAMINER TO THE BOARD OF PATENT APPEALS AND INTERFERENCES**

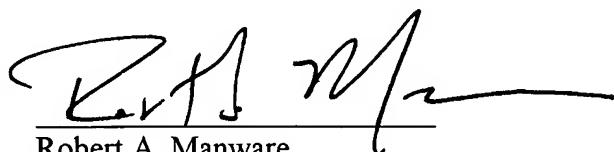
Applicants hereby appeal to the Board of Appeals and Interferences from the final decision, dated July 14, 2005, of the Examiner finally rejecting claims 12-25 of the above-referenced application.

The Commissioner is authorized to charge the requisite fee of \$500.00, and any additional fees which may be required, to Deposit Account No. 13-3092; Order No. 02-1010/FLE (MICS:0114).

**General Authorization for Extensions of Time**

In accordance with 37 C.F.R. § 1.136, Applicants hereby provide a general authorization to treat this and any future reply requiring an extension of time as incorporating a request therefor. Furthermore, Applicant authorizes the Commissioner to charge the appropriate fee for any extension of time, including the 1-month extension fee of \$120.00, to Deposit Account No. 13-3092; Order No. 02-1010/FLE (MICS:0114).

Respectfully submitted,



Date: November 14, 2005

Robert A. Manware  
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